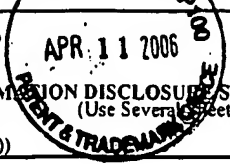


FORM PTO-1449 (Modified)				U.S. Department of Commerce Patent and Trademark Office		Attorney Docket No.: SONY-27300		Serial No.: 10/816,391	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use Several Sheets If Necessary)						Applicants: Mikhail Dorojevets et al.			
(37 CFR § 1.98(b))						Filing Date: March 31, 2004		Group Art Unit: 2613	
<b>U.S. PATENT DOCUMENTS</b>									
Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date		
/A.H./	AA	4,128,880	12/05/78	Cray, Jr.	364	200	06/30/76		
	AB								
	AC								
	AD								
	AE								
	AF								
	AG								
	AH								
	AI								
	AJ								
<b>FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS</b>									
		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation		
							Yes	No	
	AK								
	AL								
	AM								
	AN								
	AO								
<b>OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)</b>									
	AP								
	AQ								
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	AS								
	AT								
	AU								
	AV								
	AW								
	AX								
	AY								
	AZ								
Examiner: /Anner Holder/				Date Considered: 12/11/2007					
<b>EXAMINER:</b> Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									

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(Modified)

APR 19 2006

U.S. Department of Commerce  
Patent and Trademark Office

Attorney Docket No.: SONY-27300

Serial No.: 10/816,391

INFORMATION DISCLOSURE STATEMENT BY APPLICANT  
(Include Additional Sheets If Necessary)

Applicants: Mikhail Dorojevets et al.

Filing Date: March 31, 2004

Group Art Unit: 2613

(37 CFR § 1.98(b))

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	AJ						

## FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
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	AL							
	AM							
	AN							
	AO							

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/A.H./	AP	Donald Blevins and Edward Davis, "A Bit-Serial VLSI Array Processing Chip for Image Processing," pages 364-368, IEEE Journal of Solid-State Circuits, vol. 25, No. 2, April 1990					
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	AX						
	AY						
	AZ						

Examiner: /Anner Holder/

Date Considered: 12/11/2007

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